

UNITED STATES PATENT AND TRADEMARK OFFICE

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| Applicant: | TSUKADA | Examiner | BAISA, J. |
| Serial No.: | 10/552301 | Group Art: | 2832 |
| Filed: | October 7, 2005 | Docket: | 10921.0359USWO |
| Confirmation No.: | 8326 | Notice of Allow Date: | N/A |
| Due Date: | N/A | | |
| Title: | CHIP RESISTOR AND METHOD FOR MANUFACTURING SAME | | |

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PATENT TRADEMARK OFFICE

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